



Sheet 1 of 1

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) DATE MAILED: <u>May 23, 2003</u>	ATTY. DOCKET NO. 010186	SERIAL NO. 09/943,888
	APPLICANT K. Manetakis	
	FILING DATE August 30, 2001	GROUP 2819

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
	A1						
	A2						
	A3						
	A4						
	A5						
	A6						
	A7						
	A8						
	A9						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
	B1						
	B2						
	B3						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

<i>Lh</i>	C1	Eric Vittoz, "Analog VLSI Signal Processing: Why, Where and How?", Journal of VLSI Signal Processing, Vol. 8, pps 27-44 and Analog Integrated Circuits and Signal Processing, pps 27-44, July 1994
<i>L</i>	C2	Yan, et al., "Low Voltage Analog Circuit Design Techniques: A Tutorial", IEICE Trans. Analog Integrated Circuits and Systems, vol E00-A, No. 2, February 2000
	C3	
EXAMINER	DATE CONSIDERED <u>3/29/03</u>	
*EXAMINER: Initial if reference considered whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		